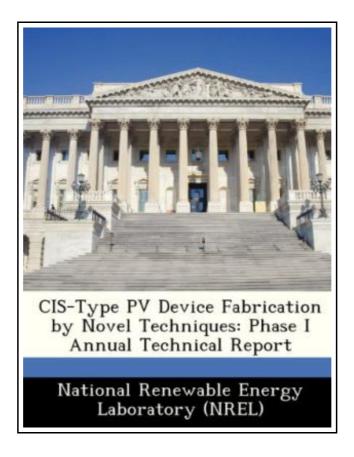
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Reviews

This publication is very gripping and exciting. Better then never, though i am quite late in start reading this one. I am very happy to inform you that here is the finest pdf i actually have read inside my very own daily life and could be he greatest publication for actually. (Dayana Aufderhar)

CIS-TYPE PV DEVICE FABRICATION BY NOVEL TECHNIQUES: PHASE I ANNUAL TECHNICAL REPORT (PAPERBACK)



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Bibliogov, United States, 2012. Paperback. Book Condition: New. 246 x 189 mm. Language: English . Brand New Book ***** Print on Demand *****. The present project was specifically geared toward developing a low-cost, non-vacuum particle deposition method for CIS-type absorber growth. There are four major processing steps in this technique: i) preparation of a starting powder containing all or some of the chemical species constituting CIS, ii) preparation of an ink using the starting powder, iii) deposition of the ink on a substrate in the form of a thin precursor layer, and iv) conversion of the precursor layer into a fused photovoltaic absorber through annealing steps. During this Phase I program, ISET worked on tasks that were geared toward the following goals: i) elimination of back-contact problems, ii) growth of large-bandgap absorbers, and iii) fabrication of mini-modules. As a result of the Phase I research, a Mo back-contact structure was developed that eliminated problems that resulted in poor mechanical integrity of the absorber layers. Sulfur inclusion into CIS films through high-temperature sulfurization in H2S gas was also studied. It was determined that S diffusion was a strong function of the stoichiometry of the CIS layer. Sulfur was found to diffuse rapidly through the Cu-rich films, whereas the diffusion constant was at least three orders of magnitude smaller in Cu-poor layers. Additionally, S profiles in sulfurized CIS films were correlated with the distribution of the grain size through the film. Absorbers containing large concentrations of Ga near the Mo contact interface also had large S content in that same region due to the small grain size of the Ga-containing material. New work on monolithic integration procedures overcame the problem of low shunt resistance and yielded CuIn(S, Se)2 (CISS) mini-modules of about 64-cm2 area with close to 7 percent efficiency.

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